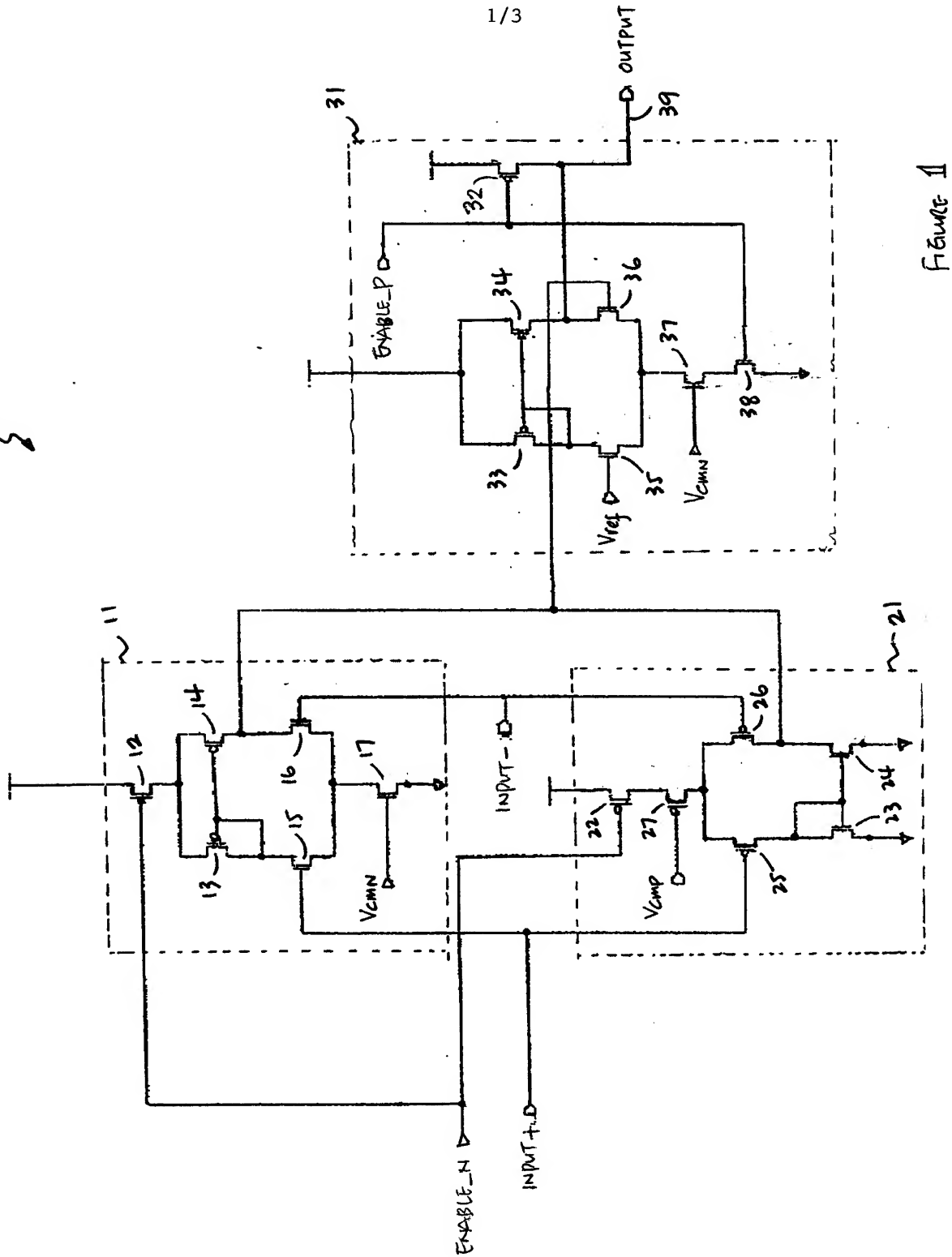
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OUTPUT
ENABLE_P
ENABLE_N
INPUT-
INPUT+
VCMN
VCMP
VREF

FIGURE 1

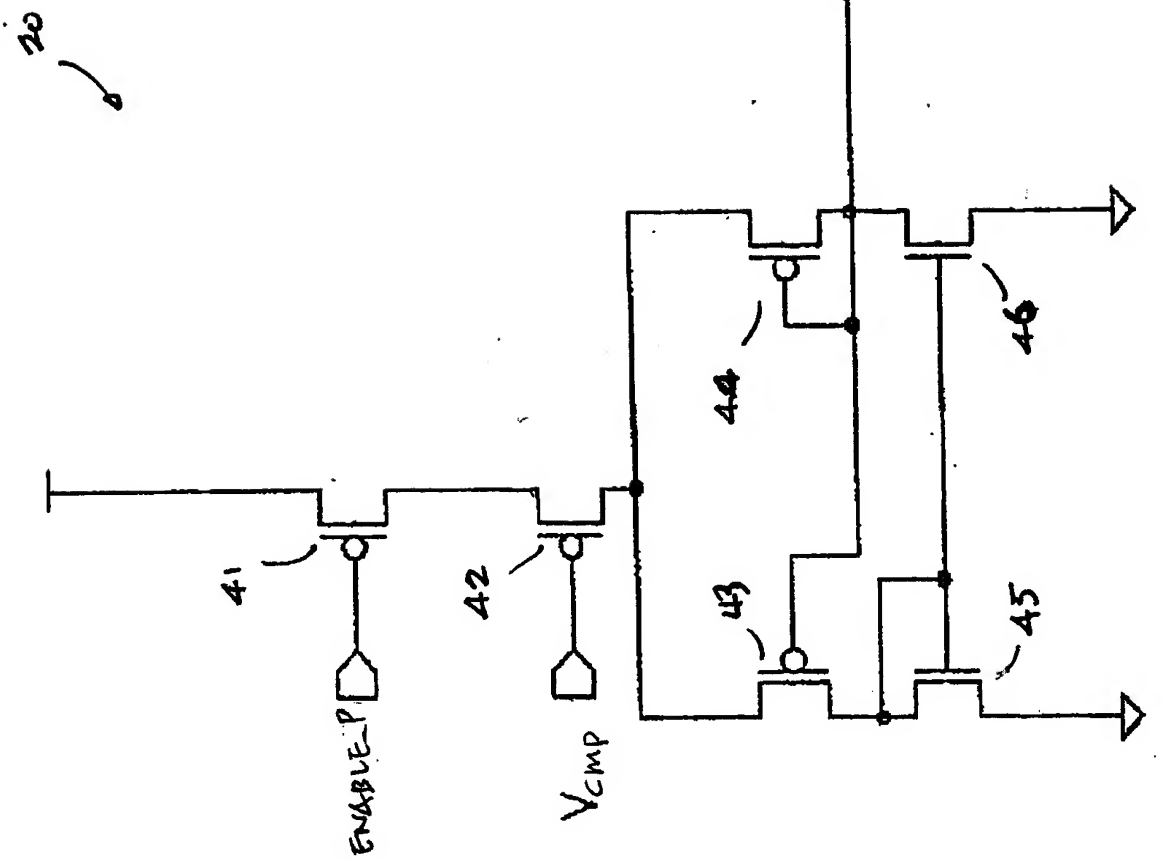
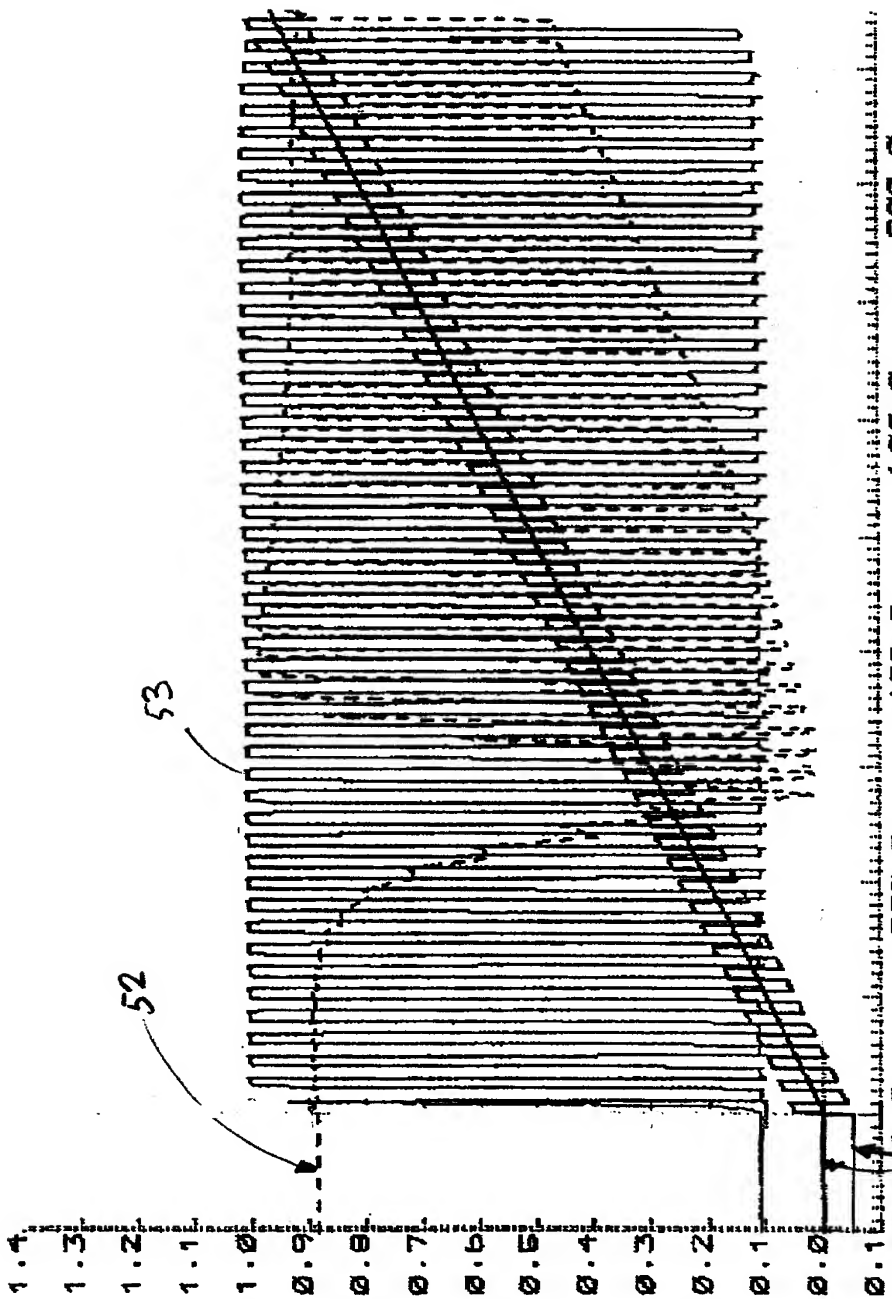


FIGURE 2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1.3
1.2
1.1
1.0
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0.0
-0.1
-100.0
0.0
200.0
400.0
600.0
800.0
51
52
53

FIGURE 3